

FIG. 1

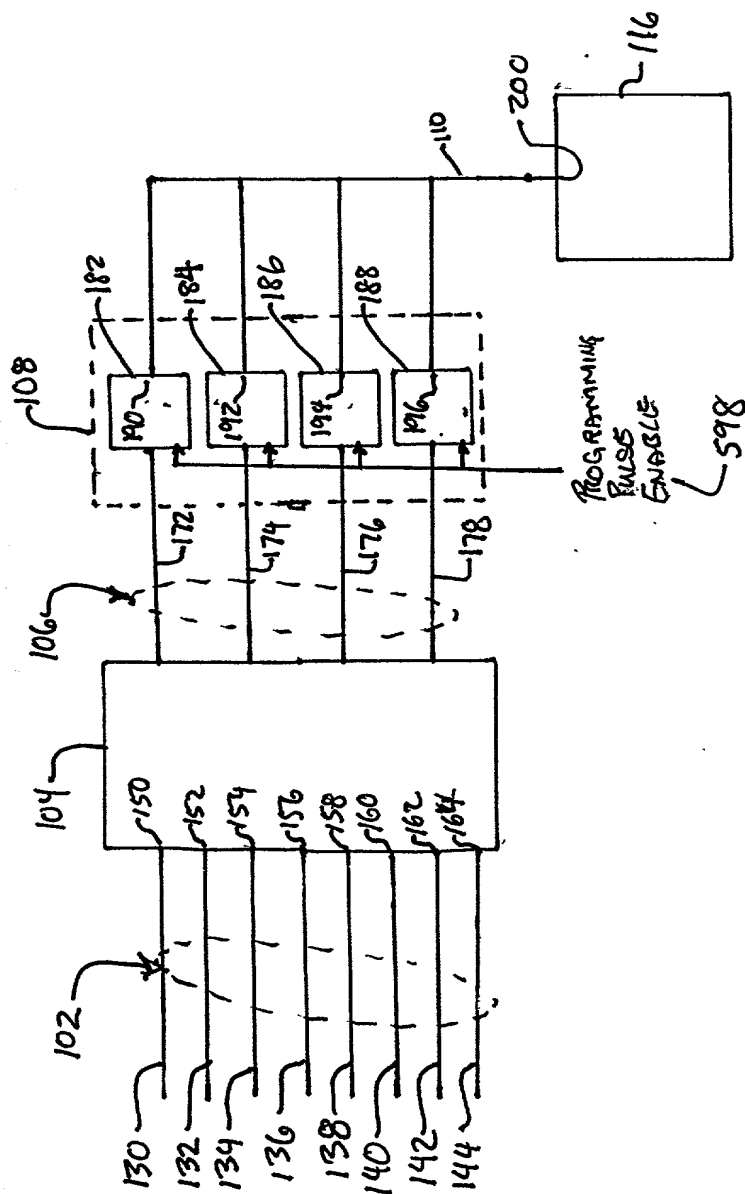


Fig. 2

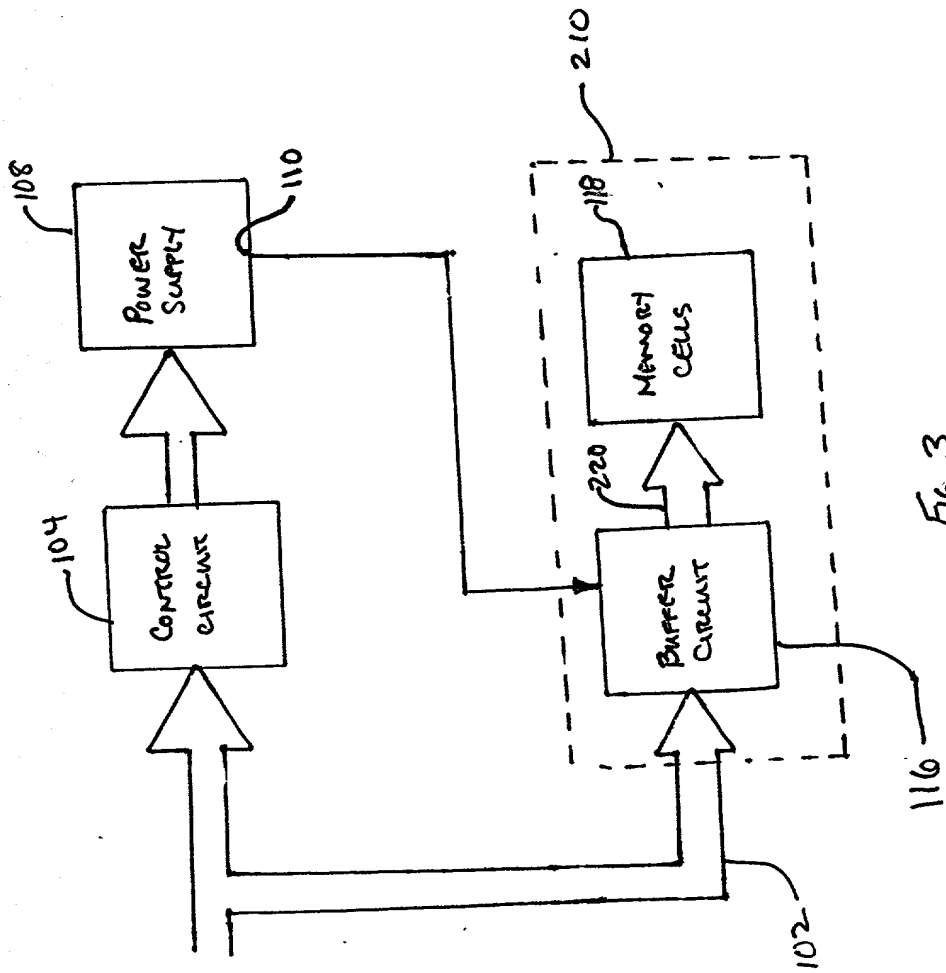


FIG. 3

FIG. 4 is a block diagram of a system 100. The system 100 includes a control circuit 104, a power supply 108, a buffer circuit 116, and memory cells 118. The control circuit 104 is connected to the power supply 108 and the buffer circuit 116. The buffer circuit 116 is connected to the memory cells 118. The system 100 is shown within a dashed box 100.

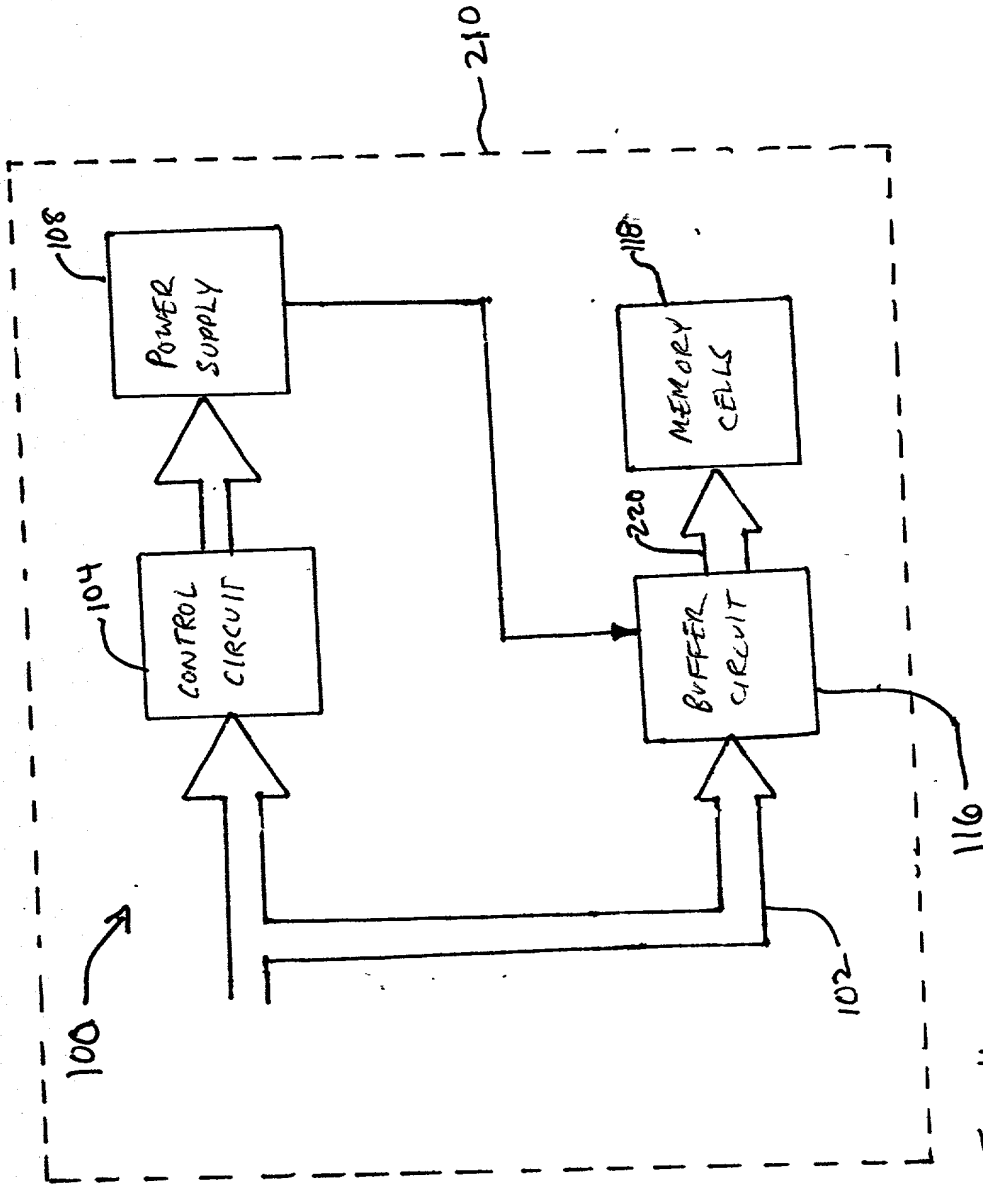
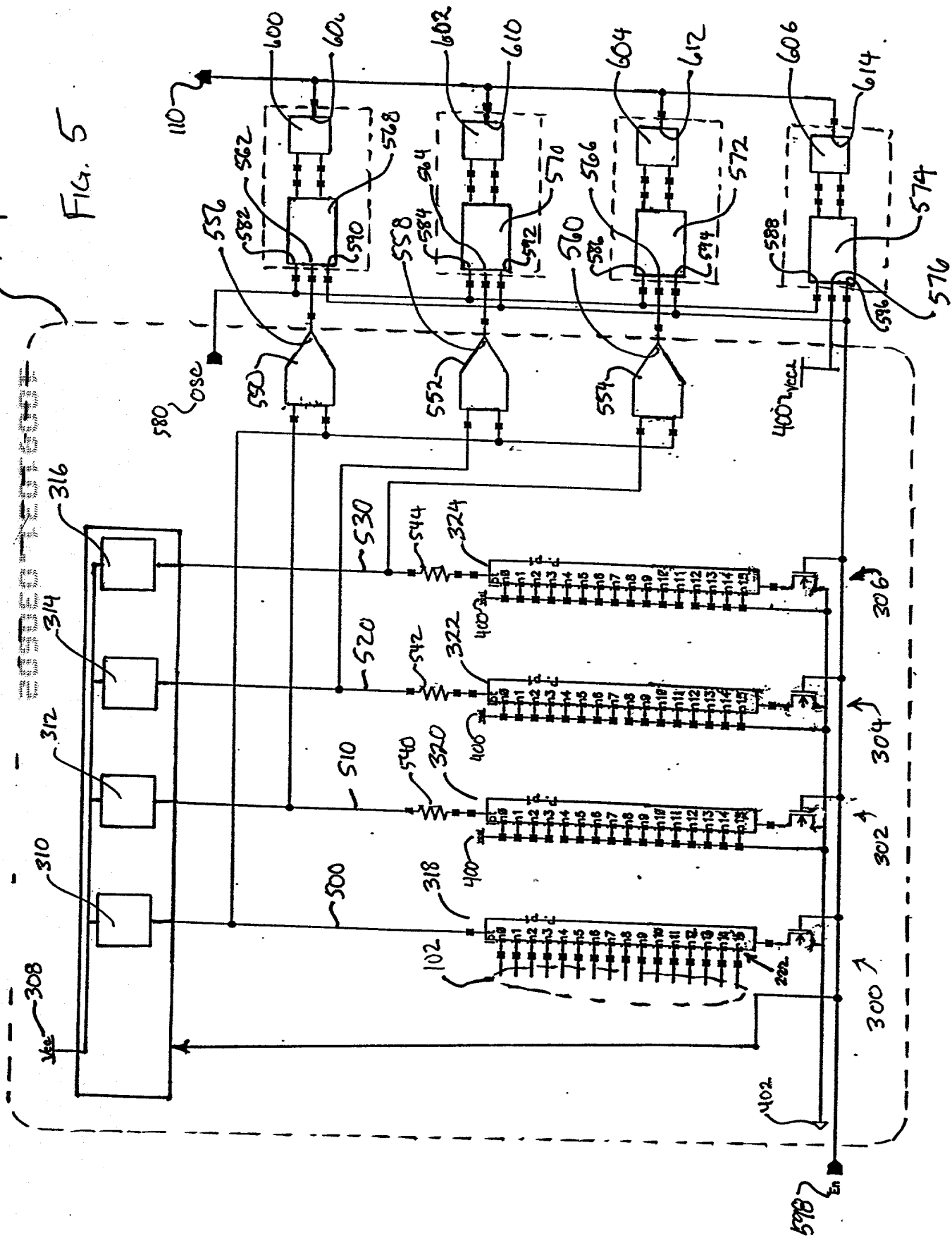


FIG. 4

104

FIG. 5



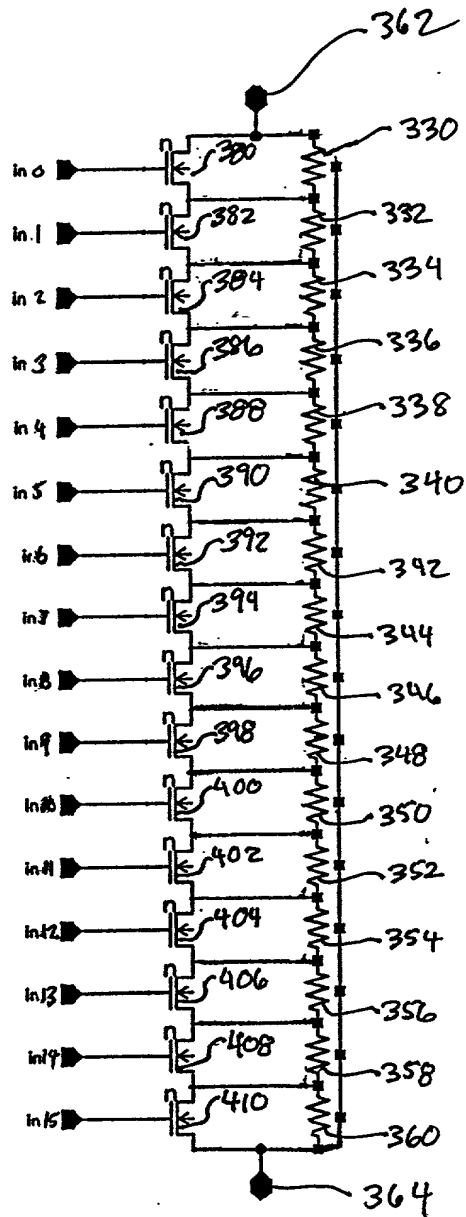
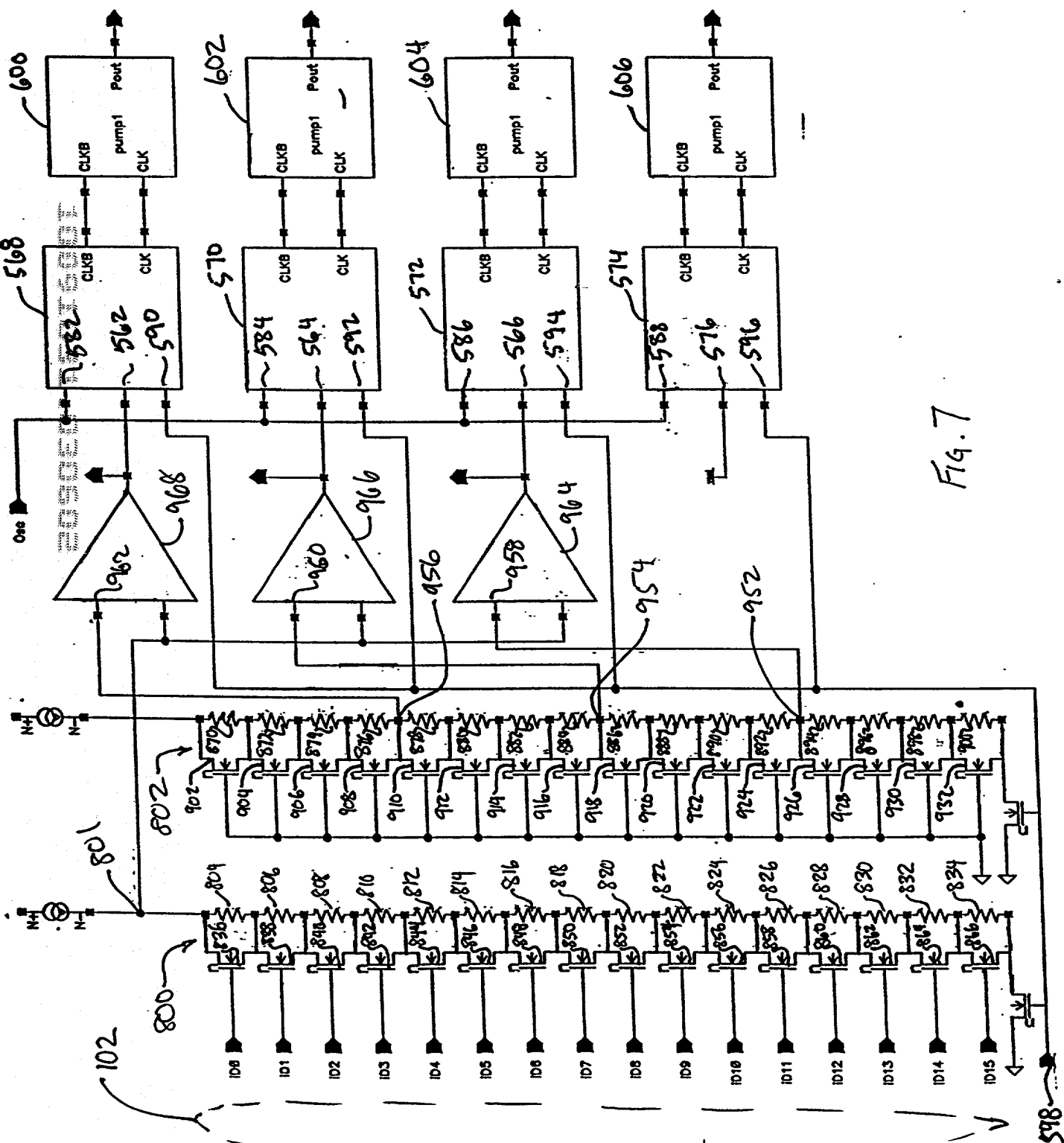


FIG. 6



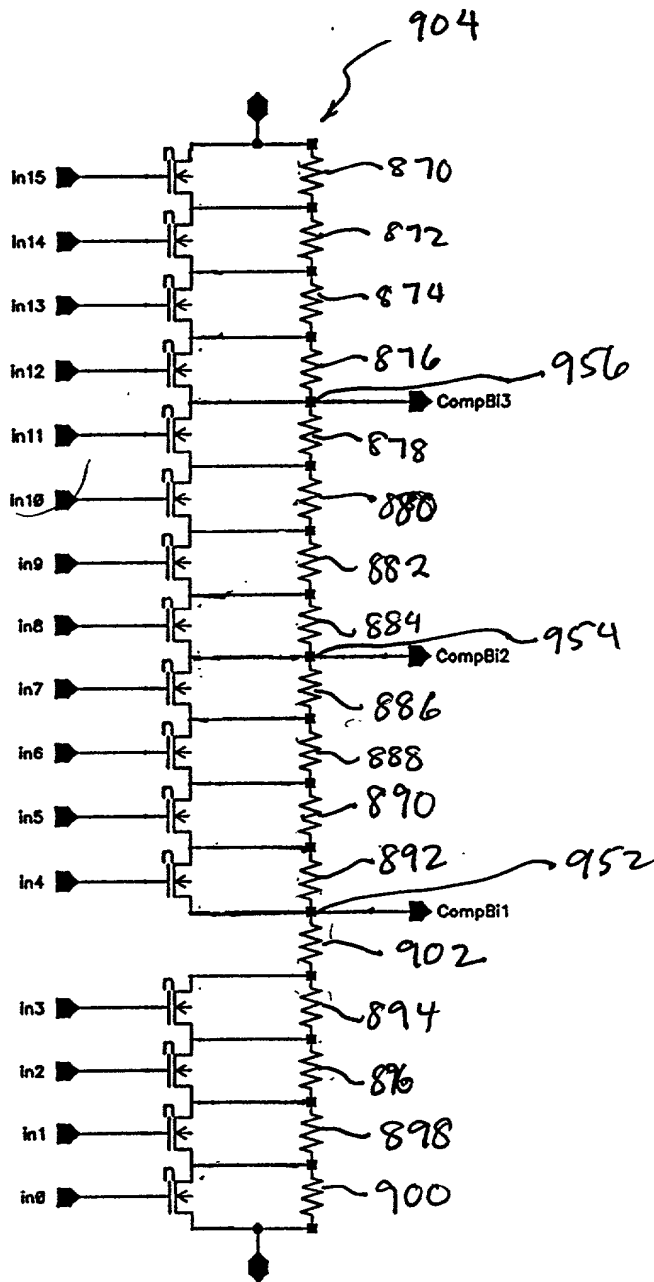


FIG. 8





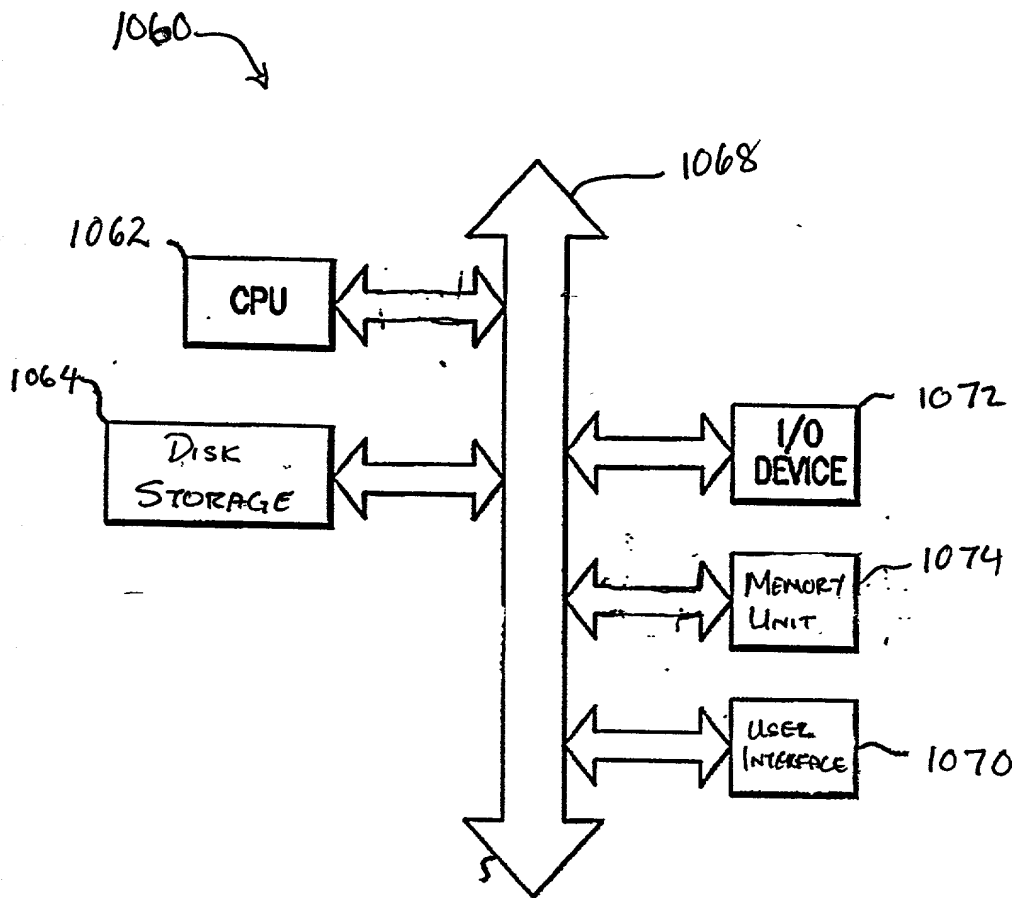


FIG. 10